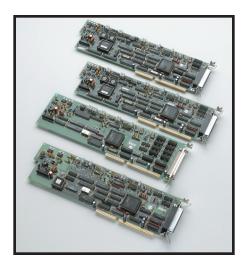
46–333kHz, 12/16-Bit Multifunction Boards

- Up to 333 kSamples/s maximum input rate
- Channel-gain queue for highspeed acquisition at different gains
- 1K word FIFO
- Programmable burst mode sampling emulates simultaneous sample-and-hold
- Pre-, post- and about-triggering
- 2 waveform-quality analog outputs (DAS-1800AO)
- 2 DC analog outputs (DAS-1800 HC and DAS-1800HR-DA)
- 32-bit DriverLINX drivers plus a suite of bundled software including ExceLINX,
 VisualSCOPE, TestPoint, and LabVIEW drivers



This family of high-performance boards provides analog and digital I/O, optimal performance with Windows, and continuous, gap-free data acquisition on up to 64 channels. With these boards you can sample a few high-speed signals, or you can sample a large number of medium-speed signals and monitor various sources and sensors at different voltage levels. Even when you use channels with different gains or in nonsequential order, these boards maintain their high-speed acquisition.

APPLICATIONS

- Product test
- Process monitoring
- Data logging



DAS-1800 SERIES SELECTOR GUIDE

| | DAS-1800AO | DAS-1800HC | DAS-1800HR | DAS-1800ST |
|---|--|--|--|--|
| Analog Inputs | 16 single-ended or 8 differential | 64 single-ended or 32 differential | 16 single-ended or 8 differential | 16 single-ended or 8 differential |
| Maximum Sampling Rate | 333 kS/s | 333 kS/s | 100 kS/s | 333 kS/s |
| Multiple Channel Aggregate Sampling Rate | 312.5 kS/s | 312.5 kS/s | 98 kS/s | 312.5 kS/s |
| Resolution | 12-bits | 12-bits | 16-bits | 12-bits |
| FIFO | 1024 locations | 1024 locations | 1024 locations | 1024 locations |
| External Expansion at Speed | Up to 256 inputs | N/A | Up to 256 inputs | Up to 256 inputs |
| Gain-Channel Queue Length | 256 | 64 | 256 | 256 |
| Gains Model 01 Model 02 | 1, 5, 50, 250 1, 2, 4, 8 | 1, 5, 50, 250 1, 2, 4, 8 | N/A 1, 2, 4, 8 | 1, 5, 50, 250 1, 2, 4, 8 |
| input Ranges–Model 01 | ±5 V, ±1 V, ±100 mV, ±20 mV 0-5 V, 0-1 V, 0-100 mV, 0-20 mV | ±5 V, ±1 V, ±100 mV, ±20 mV 0–5 V, 0–1 V, 0–100 mV, 0–20 mV | N/A | ±5 V, ±1 V, ±100 mV, ±20 mV 0–5 V, 0–1 V, 0–100 mV, 0–20 mV |
| input Ranges–Model 02 | ±10 V, ±5 V, ±2.5 V, ±1.25 V 0–10 V, 0–5 V, 0–2.5 V, 0–1.25 V | ±10 V, ±5 V, ±2.5 V, ±1.25 V 0–10 V, 0–5 V, 0–2.5 V, 0–1.25 V | ±10 V, ±5 V, ±2.5 V, ±1.25 V 0–10 V, 0–5 V, 0–2.5 V, 0–1.25 V | ±10 V, ±5 V, ±2.5 V, ±1.25 V 0–10 V, 0–5 V, 0–2.5 V, 0–1.25 V |
| D/A Outputs | 2 Waveform quality | 2 | 2 optional | 4 optional |
| O/A Update Speed | 500 kS/s | 10k typical, CPU dependent | 10k typical, CPU dependent | 10k typical, CPU dependen |
| O/A FIFO Size | 2048 Locations | 1 Location | 1 Location | 1 Location |
| Digital Inputs | 4 | 4 | 4 | 4 |
| Digital Outputs | 4 | 8 | 4 | 4 |

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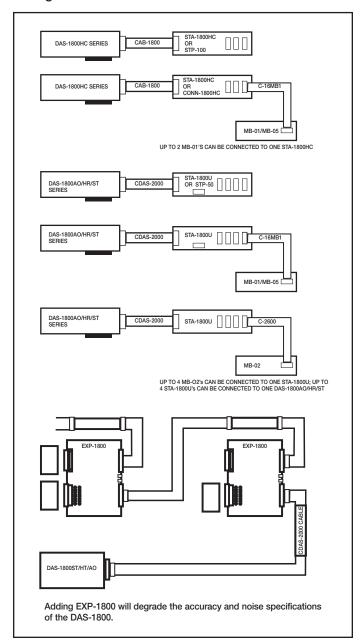
46–333kHz, 12/16-Bit Multifunction Boards

| | \sim | | |
|------------------------------------|---------------|---------------|---|
| | (| | |
| | Bank A Pin | Bank B Pin | \ |
| AGND | 1 | 1 | AGND |
| CH16 HI CH16 LO / CH48 HI | 2 3 | 2 | CH00 HI CH00 LO / CH32 HI |
| CH17HI | 4 | 4 | CH01 HI |
| CH17 LO / CH49 HI CH18 HI | 5 6 | 5 6 | CH01 LO / CH33 HI CH02 HI |
| CH18 LO / CH50 HI | 7 | 7 | CH02 LO / CH34 HI |
| CH19 HI CH19 LO / CH51 HI | 8 9 | 8 9 | CH03 HI CH03 LO / CH35 HI |
| CH20 HI | 10 | 10 | CH04 HI |
| CH20 LO / CH52 HI CH21 HI | 11 12 | 11 12 | CH04 LO / CH36 HI CH05 HI |
| CH21 LO / CH53 HI | 13 | 13 | CH05 LO / CH37 HI |
| CH22 HI CH22 LO / CH54 HI | 14 15 | 14 15 | CH06 HI CH06 LO / CH38 HI |
| CH23 HI | 16 | 16 | CH07 HI |
| CH23 LO / CH55 HI AGND | 17 18 | 17 18 | CH07 LO / CH39 HI AGND |
| CH24HI | 19 | 19 | CH08 HI |
| CH24 LO / CH56 HI CH25 HI | 20 21 | 20 21 | CH08 LO / CH40 HI CH09 HI |
| CH25 LO / CH57 HI | 22 | 22 | CH09 LO / CH41 HI |
| CH26 HI CH26 LO / CH58 HI | 23 24 | 23 24 | CH10 HI CH10 LO / CH42 HI |
| CH27 HI | 25 | 25 | CH11 HI |
| CH27 LO / CH59 HI CH28 HI | 26 27 | 26 27 | CH11LO/CH43HI CH12HI |
| CH28 LO / CH60 HI | 28 | 28 | CH12LO / CH44 HI |
| CH29 HI CH29 LO / CH61 HI | 29 30 | 29 30 | CH13HI CH13LO/CH45HI |
| CH30 HI | 31 | 31 | CH14HI |
| CH30 LO / CH62 HI CH31 HI | 32 33 | 32 33 | CH14LO / CH46 HI CH15 HI |
| H31 LO / CH63 HI | 34 | 34 | CH15LO / CH47HI |
| AGND DAC1 OUT | 35 36 | 35 36 | AGND DACCOUT |
| -15 V | 37 | 37 | +15 V |
| DGND NC | 38 39 | 38 39 | DGND DI0 / XPCLK |
| SSHO | 40 | 40 | DI1 / TGIN |
| TGOUT DOSTB | 41 42 | 41 42 | DI2 DI3 |
| DO4 | 43 | 43 | D00 |
| DO5 DO6 | 44 45 | 44 45 | DO1 DO2 |
| DO7 | 46 | 46 | D03 |
| +5 V +5 V | 47 48 | 47 48 | +5 V +5 V |
| DGND | 49 | 49 | DGND |
| DGND | 50 | 50 | DGND |
| | | | |
| | DAS-1 | 800HC | |
| | | | |
| | | | |
| | | _ | |
| | Pin | Pin | \ |
| (User CommonMode)U_CMMD | 1 | 26 | СНООНІ |
| CH00LOor CH08HI CH01LOor CH09HI | 2 3 | 27 28 | CH01HI CH02HI |
| CH02LOor CH10HI | 4 | 29 | CH03HI |
| CH03LOor CH11HI CH04LOor CH12HI | 5 6 | 30 31 | CH04HI CH05HI |
| CH05LOor CH13HI | 7 | 32 | CH06HI |
| CH06LOor CH14HI CH07LOor CH15HI | 8 9 | 33 34 | CH07HI LLGND |
| (DAS-1800ST-DA) 0DAC 2 | 10 | 35 | 0DAC 0 (DAS-1800AO/HR-DA/ST-DA) |
| (DAS-1800ST-DA) 0DAC 3 +15V | 11 12 | 36 37 | 0DAC 1 (DAS-1800AO/HR-DA/ST-DA) -15V |
| LLGND | 13 | 38 | LLGND |
| DGND DI1 | 14 15 | 39 40 | GEXT DI0 |
| DI3 | 16 | 41 | DI2 |
| DO1 DO3 | 17 18 | 42 43 | D00 D02 |
| DOSTB | 19 | 44 | XPCLK |
| TGOUT MUX03 | 20 21 | 45 46 | SSHO TGIN |
| MUX05 | 22 | 47 | MUX04 |
| MUX07 +5V | 23 24 | 48 49 | MUX06 +5V |
| DGND | 25 | 50 | DGND |
| | | | |
| | | | |
| | DAS-1 | 800HR/ | ST |
| | | | |

Connector Pin Assignments

The analog input, analog output, and digital input and output connections of the DAS-1800AO, DAS-1800HR, and DAS-1800ST are made with a 50-pin D-type connector at the rear of the computer. The analog input, analog output, and digital input and output connections of the DAS-1800HC are made with a 100-pin D-type connector at the rear of the computer.

Configuration Guide



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46–333kHz, 12/16-Bit Multifunction Boards

Analog Inputs (DAS-1800AO/HC/ST)

NUMBER OF CHANNELS:

8 differential or 16 single-ended; software-configurable with software selectable remote sense (DAS-1800AO/ST)

32 differential or 64 single-ended; software-configurable (DAS-1800HC).

A/D FIFO BUFFER SIZE: 1024 words.

CHANNEL/GAIN QUEUE LENGTH:

256 locations (DAS-1800AO/ST); 64 locations (DAS-1800HC)

RESOLUTION: 12 bits

INPUT GAINS: DAS-1801AO/HC/ST: 1, 5, 50, 250. DAS-1702AO/ST, DAS-1802AO/HC/ST: 1, 2, 4, 8.

| INPUT RANGES: | BIPOLAR | UNIPOLAR |
|------------------|---------|-------------------|
| DAS-1801AO/HC/ST | ±20 mV | 0-100 mV, 0-20 mV |
| DAS-1802AO/HC/ST | ±1.25 V | 0-2.5 V, 0-1.25 V |

INPUT RANGE SELECTION: Software programmable.

INPUT OVERVOLTAGE: ±15V continuous, powered; ±15V continuous, unpowered.

INPUT BIAS CURRENT: ±40nA max. @ 25°C; ±60nA max. over operating temperature.

INPUT IMPEDANCE: $> 100 \text{M}\Omega$ in parallel with 90pF.

THROUGHPUT-SINGLE CHANNEL: 333kS/s for any gain or range.

THROUGHPUT-DAS-1701/AO/ST, DAS-1801AO/HC/ST

(multiple channels, at the same gain):

| GAIN | BIPOLAR INPUTS | UNIPOLAR INPUTS | |
|------|----------------|-----------------|--|
| 1 | 312.5 kS/s | 312.5 kS/s | |
| 5 | 312.5 kS/s | 312.5 kS/s | |
| 50 | 312.5 kS/s | 200 kS/s | |
| 250 | 75 kS/s | 60 kS/s | |

THROUGHPUT-DAS-1802AO/HC/ST (multiple channels, at the same gain): 312.5kS/s for all

LINEARITY: Integral: ±½ LSB typical, ±1 LSB max. Differential: ±1 LSB.

ABSOLUTE ACCURACY:

±0.01% of reading ±1 LSB for all ranges, typical.

±0.02% of reading ±1 LSB for gain < 250, max. @ 25°C.

 $\pm 0.03\%$ of reading ± 1 LSB for gain = 250, max. @ 25°C.

TEMPERATURE COEFFICIENTS:

Offset - unipolar: $\pm 10\mu\text{V/}^{\circ}\text{C} \pm (14\mu\text{V/}^{\circ}\text{C} \div \text{gain}) \text{ max}$.

Offset - bipolar: $\pm 10\mu\text{V/°C} \pm (12\mu\text{V/°C} \div \text{gain}) \text{ max}$.

GAIN: Gain < 50: ± 20 ppm/°C of FS max.

Gain = 50: ± 30 ppm/°C of FS max. Gain = 250: ± 35 ppm/°C of FS max.

CONVERSION TIME: 3.0µs max.

NOISE (DAS-1801AO/HC/ST):

| Gain | Bipolar (counts) | Unipolar (counts) | |
|------|----------------------|----------------------|--|
| 1 | p-p = 1; $rms = 0.1$ | p-p = 1; rms = 0.1 | |
| 5 | p-p = 1; rms = 0.1 | p-p = 1; $rms = 0.1$ | |
| 50 | p-p = 4; rms = 0.5 | p-p = 6; rms = 0.9 | |
| 250 | p-p = 8; rms = 1.0 | p-p = 9; $rms = 1.4$ | |

NOISE (DAS-1802AO/HC/ST): p-p = 1; rms = 0.1, for all gains and ranges. 74dB.

COMMON MODE REJECTION RATIO: Gain = 1: Gain = 2, 4, 5:

80dB Gain = 8:86dB. Gain =1 50, 250: 100dB

DATA TRANSFER MODES: DMA (single or dual channel), interrupt, or programmed I/O.

Analog Inputs (DAS-1800HR)

NUMBER OF CHANNELS: 8 differential or 16 single-ended; software-configurable with software selectable remote sense

A/D FIFO BUFFER SIZE: 1024 locations

CHANNEL/GAIN QUEUE LENGTH: 256 locations.

RESOLUTION: 16 bits. **INPUT GAINS:** 1, 2, 4, 8.

INPUT RANGES:

Bipolar: $\pm 10V$, $\pm 5V$, $\pm 2.5V$, $\pm 1.25V$.

Unipolar: 0 to + 10V, 0 to +5V, 0 to +2.5V, 0 to +1.25V

INPUT RANGE SELECTION: Software programmable.

INPUT OVERVOLTAGE: ±15V continuous, powered; ±15V continuous, unpowered.

INPUT BIAS CURRENT: ±40nA max. @ 25°C; ±60nA max. over operating temperature.

INPUT IMPEDANCE: $> 100 \text{M}\Omega$ in parallel with 90pF.

THROUGHPUT, SINGLE CHANNEL: 100kS/s for any gain or range.

THROUGHPUT, MULTIPLE CHANNELS: 98kS/s, at the same gain, all ranges.

THROUGHPUT, MULTIPLE CHANNELS: 60kS/s, with gain change.

LINEARITY DIFFERENTIAL: ±1 LSB. Monotonicity guaranteed over operating range.

ABSOLUTE ACCURACY:

 $\pm 0.005\%$ of reading ± 1 LSB. Typical, all ranges: Maximum for gain = 1: $\pm 0.005\%$ of reading ± 1.5 LSB. Maximum for gain > 1: ±0.001% of reading ±1.5 LSB.

RELATIVE ACCURACY:

Typical: ±0.001% of reading ±1 LSB.

Maximum for 0-70°C: ±0.001% of reading ±1.5 LSB.

TEMPERATURE COEFFICIENTS:

Offset: ±5µV/°C max

Gain: ± 7.5ppm/°C of FS max.

Conversion time: 8µs max

| Gain | Bipolar (counts) | Unipolar (counts) | |
|------|-------------------------------|-------------------------------|--|
| 1 | $p-p = \pm 2$; rms = 0.5 | $p-p = \pm 2$; $rms = 0.6$ | |
| 2 | $p-p = \pm 2$; rms = 0.5 | $p-p = \pm 2$; $rms = 0.6$ | |
| 4 | $p-p = \pm 2.5$; $rms = 0.6$ | $p-p = \pm 2.5$; $rms = 0.7$ | |
| 8 | $p-p = \pm 2.5$; rms = 0.7 | $p-p = \pm 3$; rms = 0.8 | |

COMMON MODE REJECTION RATIO:

Gain = 1: 74dB 80dB. Gain = 2.4:Gain = 8: 86dB

DATA TRANSFER MODES: DMA (single or dual channel), interrupt, or programmed I/O.

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46–333kHz, 12/16-Bit Multifunction Boards

Analog Outputs

(DAS-1800AO only)

NUMBER OF CHANNELS: 2.

RESOLUTION: 12 bits.

RANGE: ±5V ±10V software programmable

FIFO: 2048 words.

OUTPUT DRIVE CURRENT: ±15mA max.

CAPACITIVE LOAD DRIVE: 100µF max.

GAIN ACCURACY: Adjustable to zero.

OFFSET ACCURACY: Adjustable to zero. LINEARITY: Integral: ±0.25 LSB typical, ±0.75 LSB max.

Differential: ±0.75 LSB max.

POWER-UP STATE: 0.0V

SAMPLE CLOCK: Internal clock, 7S/s to 500kS/s, A/D clock or

external clock

SETTLING TIME: $3\mu s$ for 20V step, typical; $1.8\mu s$ for LSB of

major carry, typical.

THROUGHPUT: 500kS/s max., per channel in recycle mode.

GLITCH ENERGY: Zero glitch feedthrough

TRIGGER SOURCES: Internal, external trigger, or external gate. DATA TRANSFER MODES: DMA, interrupt, or programmed I/O.

ANALOG OUTPUTS

(DAS-1800HC ONLY)

NUMBER OF CHANNELS: 2.

RESOLUTION: 12 bits.

RANGE: ±10V.

ABSOLUTE ACCURACY: ±1 LSB max.

OUTPUT DRIVE CURRENT: ±5mA max.

CAPACITIVE LOAD DRIVE: 100uF

LINEARITY: Integral: ±1/4 LSB typ.; ±1/2 LSB max.

POWER-UP STATE: 0.0V.

GLITCH ENERGY: 300nV-seconds.

DATA TRANSFER MODES: Interrupt or programmed I/O.

ENVIRONMENTAL

OPERATING TEMPERATURE: °C to +50°C

STORAGE TEMPERATURE: -20°C to +70°C

HUMIDITY: 0 to 95% (non-condensing)

EMC: Conforms to European Union Directive 89/336/EEC.

SAFETY: Meets EN61010-1/IEC 1010.

DIMENSIONS: 13.3in L × 4.25in H × 0.75in D (33.8cm × 10.8cm

ACCESSORIES AVAILABLE

C2600 26-inch ribbon cable for the MB Series signal conditioning rack C-16MB1 MB-01 backplane to STA-1800HC or STA-1800U cable DAS-1800HC to STA-1800HC 100-pin, CAB-1800 18-inch cable CAB-1800/S DAS-1800HC to STA-1800HC 100-pin, 18-inch shielded cable CAB-1801/S DAS-1800HC to STA-1800HC 100-pin, 36-inch shielded cable

CAB-1802/S DAS-1800HC to STA-1800HC 100-pin, 72-inch shielded cable

120 inch shielded cable

CAB-1803/S DAS-1800AO, DAS-1800HR, or DAS-1800ST to CDAS-2000

STA-1800U cable

CONN-1800HC Connector Accessory for the DAS-1800HC EXP-1800 Signal Conditioning and Expansion Accessory

MB-01* 16-Channel Direct-Connection Module

Mounting Rack

16-Channel Multiplexed Module Mounting Rack

MB-05* 8-Channel Direct-Connection Module

Mounting Rack

STA-1800HC Screw Terminal Accessory for the DAS-1800HC

Series w/CJC for Thermocouples Universal Screw Terminal Accessory for the

STA-1800U DAS-1800AO, DAS-1800HR, and DAS-1800ST Screw Terminal Panel for 100-pin connectors STP-100

Screw Terminal Panel for 50-pin connectors STP-50 TESTPOINT TestPoint Software Package

*Signal conditioning modules for the MB-01, MB-02, and MB-05 can be found in the Signal Conditioning and Accessories section.

Ordering Information

DAS-1801AO

333 kS/s Analog and Digital I/O Board with gains of 1, 5, 50, 250 and two waveform quality analog outputs

DAS-1802AO

333 kS/s Analog and Digital I/O Board with gains of 1, 2, 4, 8 and two waveform quality analog outputs

DAS-1801HC

High Channel Count 333 kS/s Analog and Digital I/O Board with gains of 1, 5, 50, 250

DAS-1802HC

High Channel Count 333 kS/s Analog and Digital I/O Board with gains of 1, 2, 4, 8

DAS-1802HR

High Resolution 100 kS/s Analog and Digital I/O Board with gains of 1, 2, 4, and 8

DAS-1802HR-DA

High Resolution 100 kS/s Analog and Digital I/O Board with gains of 1, 2, 4, and 8 and two analog outputs

DAS-1801ST

Standard 333 kS/s Analog and Digital I/O Board with gains of 1, 5, 50, 250

DAS-1801ST-DA

Standard 333 kS/s Analog and Digital I/O Board with gains of 1, 5, 50, 250 and four analog outputs

DAS-1802ST

Standard 333 kS/s Analog and Digital I/O Board with gains of 1, 2, 4, 8

DAS-1802ST-DA

Standard 333 kS/s Analog and Digital I/O Board with gains of 1, 2, 4, 8 and 4 analog outputs

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